

WHAT IS CLAIMED IS:

1. An analog circuit comprising a first transistor, a first capacitor, a first switch, a first terminal, a second terminal, a second transistor, a second capacitor, a second switch, a third terminal, and a fourth terminal,

5            wherein a gate terminal of the first transistor and one terminal of the first capacitor are electrically connected;

             wherein a gate terminal of the second transistor and one terminal of the second capacitor are electrically connected;

             wherein a source terminal of the first transistor and a source terminal of the second  
10   transistor are electrically connected;

             the first terminal and one terminal of the first capacitor are electrically connected via the first switch; and

             the third terminal and one terminal of the second capacitor are electrically connected via the second switch, comprising

15            a unit for electrically connecting the other terminal of the first capacitor and either of the second terminal or a source terminal of the first transistor; and

             a unit for electrically connecting the other terminal of the second capacitor and either of the fourth terminal or a source terminal of the second transistor.

20            2. The analog circuit according to claim 1, comprising a unit for supplying current,

             wherein the source terminal of the first transistor and the unit for supplying current are electrically connected.

             3. The analog circuit according to claim 1, comprising

25            a unit for interrupting current supplied to the first transistor; and

a unit for interrupting current supplied to the second transistor.

4.The analog circuit according to claim 1,

wherein the first terminal and the second terminal are electrically connected; and

5 wherein the third terminal and the fourth terminal are electrically connected.

5.The analog circuit according to claim 1, which forms any one of a differential amplifier circuit, an operational amplifier, and a signal driver circuit.

10 6.A display device provided with the analog circuit set forth in claim 1.

7.An electronic device provided with a display device set forth in claim 6.